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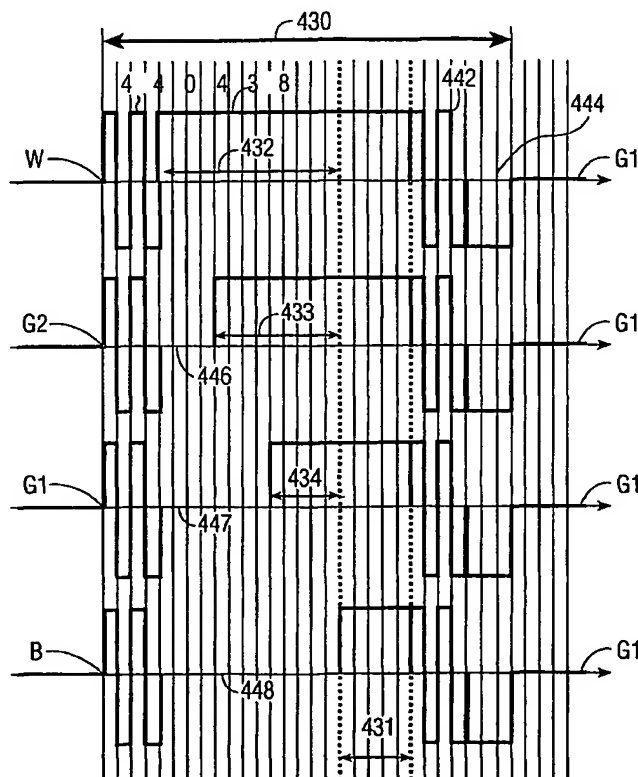
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[Continued on next page]

(54) Title: **BI-STABLE DISPLAY WITH DC-BALANCED OVER-RESET DRIVING**



PRIOR ART

(57) Abstract: A display device (101) has groups of display elements (118), which are changed from one optical state to another optical state by applying a waveform sequence of potential differences. The waveform enables particles (108, 109) to occupy a position corresponding to the other optical state and includes standard reset, over-reset and grayscale drive. The standard reset part of the waveform applies a potential difference, which is proportional to a distance the particles (108, 109) must move to reach one of the extreme optical states and the over-reset is independent of the distance. Grayscale or color scale accuracy is improved and direct charge on a pixel may be balanced over time with consequent grayscale drift compensated by tuning the grayscale driving pulse.



MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.

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**Declarations under Rule 4.17:**

- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA,

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- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for all designations
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